

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Hosein, et al.

Serial No.: 10/731,909

Filed: December 10, 2003

For: **PER USER RATE CONTROL FOR THE
REVERSE LINK IN CDMA NETWORKS**

Attorney's Docket No.: 4740-231



Examiner TBA
Group Art Unit TBA

Raleigh, North Carolina
May 6, 2004

Mail Stop Patent Applications
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Information Disclosure Statement


Sir:

Attached hereto is an information disclosure statement (PTO-1449). This IDS is being submitted before any action on the merits. As such, no fee is required. However, if fees are required, please deduct from our Deposit Account No. 18-1167.

Respectfully submitted,

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I HEREBY CERTIFY THAT THIS DOCUMENT IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL, POSTAGE PREPAID, ON THE DATE INDICATED, IN AN ENVELOPE ADDRESSED TO MAIL STOP PATENT APPLICATION, COMMISSIONER FOR PATENTS, P.O. BOX 1450, WASHINGTON, DC 22313-1450.

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FORM PTO-1449

**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION PURSUANT TO
37 CFR §1.97 & 1.98**Docket Number: | Serial Number:
| 4740-231 | 10/731.909Applicant:
| Hosein, et al.
Filing Date: | Group:
| December 10, 2003 |**U. S. PATENT DOCUMENTS**

Examiner

Filing Date

Initial	Patent No.	Date	Name	Class	Subcl	If Approp.

FOREIGN PATENT DOCUMENTS

Translation

Patent No.	Date	Name	Class	Subcl	Yes	No

OTHER DOCUMENTS (including author, title, date, pages, etc.)

no date		telelogic; Fareeq Khan; System Performance of Lucent's HRPD
no date		proposal; C30-20030915-217; 3 pgs.
		electronics; Kwon, et al.; Description of Scheduling and Resource
		or Various Control Mechanisms simulated by Samsung under HARQ
		30-WG3-20030812-071; 14 pgs.
/QV/		transmission with distributed scheduling using geometry based-priority function;
		August 12 2003; C30-20030812-061-QCOM; 4 pgs.
/QV/	D	LG Electronics; Kim, et al.; Rate Control Mechanism for LGE's Reverse Link
		Framework Proposal for Revision D; August 6, 2003; C30-2203-806-021; 9 pgs.
/QV/	E	Nokia; Pi, et al.; Cdma2000 Rev.D Performance with 10ms 3 HARQ
		Transmission and Full Buffer Traffic; August 6, 2003; C30-20030806-042; 9 pgs.
no date	F	Nortel Networks; Vrzic, et al.; Full Buffer Simulation Results for Semi distributed
		Scheduling, with 10ms Frame and 3 Transmissions; C30-2003-806-052R1; 2 pgs.

EXAMINER

/Quochien Vuong/

DATE CONSIDERED

05/12/2008

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation not in conformance and not considered. Include copy of this form with next communication to the applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /QV/